

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Peter J. Hopper et al.

Application No.: 10/647,604

Filed: August 25, 2003

For: ULTRA LOW LEAKAGE MOSFET
TRANSISTOR

Confirmation No.: 2816

Group Art Unit: 2826

Examiner: Johannes P. Mondt

**RESPONSE TO OFFICE ACTION
MAILED DECEMBER 30, 2005**353 Sacramento Street, Suite 2200
San Francisco, CA 94111
Telephone: (415) 772-4900
Facsimile: (415) 398-2890Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**CERTIFICATE OF MAILING**I hereby certify that this correspondence is being deposited
with the United States Postal Service as First Class Mail in an
envelope, addressed to: Commissioner for Patents, P.O.
Box 1450, Alexandria, VA 22313-1450 on May 18, 2006.

STALLMAN & POLLOCK LLP

Dated: 05/18 /06By: Georgia K. Stith

Georgia K. Stith

Sir:

In response to the Office Action mailed December 30, 2005, and in conjunction with Applicants' request for a two-month extension of time, please amend the above-identified application as follows:

Amendment to the Claims are provided in the Listing of Claims that begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

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